

## **IN THE CLAIMS**

This listing of the claim will replace all prior versions and listings of claim in the present application.

### **Listing of Claims**

1. (currently amended)An image display system having:
    - an image display unit; and
    - a control unit for outputting ~~the~~ image information to said image display unit, wherein said control unit comprises:
      - a block discrimination circuit portion for discriminating a state of said image information amounting to one frame among the image information in a pixel block unit;<sub>1</sub>
      - an image processing portion for processing said pixel block unit including said image information based on ~~the basis of the~~ a discriminated result of said block discrimination circuit portion;<sub>1</sub>
      - a storage portion for storing ~~the~~said image information processed by said image processing portion;<sub>1</sub> and
      - a synchronizing signal generation portion for reading ~~the~~said image information from said storage portion, controlling ~~the~~ a clock in accordance with ~~said~~ the read image information, and outputting the read image information to said image display unit;<sub>1</sub>
- wherein said image processing portion processes said pixel block unit such that either each of a plurality of pixel block areas included in said pixel block unit is

rewritten for each of a plurality of frames, or each pixel block area remains the same for a plurality of said frames.

2. (currently amended)The image display system according to claim 1, wherein said block discrimination circuit portion discriminates ~~the state of whether~~ said image information is at least ~~the a~~ moving picture or the still picture in said pixel block unit.

3. (currently amended)The image display system according to claim 2, wherein said image processing portion processes the image information differently depending on the discriminated result ~~of the state in said pixel block unit.~~

4. (original)The image display system according to claim 3, wherein said image processing portion processes the image information at a smaller number of gradations when said discriminated result is the moving picture than when said discriminated result is the still picture.

5. (original) The image display system according to claim 2, wherein said synchronizing signal generation portion controls the clock differently depending on the discriminated result of the state in said pixel block unit.

6. (original) The image display system according to claim 5, wherein said synchronizing signal generation portion generates a faster clock in the clock

control when said discriminated result is the moving picture than when said discriminated result is the still picture.

7. (currently amended)A television receiver having:

- an image display unit; and
- a control unit for outputting ~~the~~ image information to said image display unit, wherein said control unit comprises:
  - a receiving portion for receiving ~~the~~ said image information;<sub>1</sub>
  - a block discrimination circuit portion for discriminating a state of image information amounting to one frame among the image information received by said receiving portion in a pixel block unit;<sub>1</sub>
  - an image processing portion for processing said pixel block unit including said image information based on ~~the basis of the~~ a discriminated result of said block discrimination circuit portion;<sub>1</sub>
  - a storage portion for storing ~~the~~ said image information processed by said image processing portion;<sub>1</sub> and
  - a synchronizing signal generation portion for reading ~~the~~ said image information from said storage portion, controlling ~~the~~ a clock in accordance with ~~said~~ the read image information, and outputting the read image information to said image display unit;<sub>7</sub>

wherein said image processing portion processes said pixel block unit such that either each of a plurality of pixel block areas included in said pixel block unit is

rewritten for each of a plurality of frames, or each pixel block area remains the same for a plurality of frames.

8. (currently amended)The image display system according to claim 7, wherein said block discrimination circuit portion discriminates ~~the state of~~whether said image information is at least ~~the~~a moving picture or ~~the~~a still picture in said pixel block unit.

9. (currently amended)An information processing device having:  
an image display unit; and  
a control unit for outputting ~~the~~ image information to said image display unit, wherein said control unit comprises:  
a CPU for generating the image information;<sub>1</sub>  
a block discrimination circuit portion for discriminating a state of said image information amounting to one frame among the image information generated by said CPU in a pixel block unit;<sub>1</sub>  
an image processing portion for processing said pixel block unit including said image information based on ~~the basis of the~~a discriminated result of said block discrimination circuit portion;<sub>1</sub>  
a storage portion for storing ~~the~~said image information processed by said image processing portion;<sub>1</sub> and  
a synchronizing signal generation portion for reading ~~the~~said image information from said storage portion, controlling ~~the~~a clock in accordance with said

the read image information, and outputting the read image information to said image display unit;

wherein said image processing portion processes said pixel block unit such that either each of a plurality of pixel block areas included in said pixel block unit is rewritten for each of a plurality of frames, or each pixel block area remains the same for a plurality of said frames.

10. (currently amended)The image display system according to claim 9, wherein said block discrimination circuit portion discriminates ~~the state of~~whether said image information is at least ~~the a~~ moving picture or ~~the a~~ still picture in said pixel block unit.

11. (currently amended)A transmitter for transmitting image information, comprising:

a block discrimination circuit portion for discriminating a state of image information amounting to one frame in a pixel block unit;

an image processing portion for processing said pixel block unit including said image information based on the basis of the a discriminated result of said block discrimination circuit portion; and

a transmitting portion for transmitting said image information;

wherein said image processing portion processes said pixel block unit such that either each of a plurality of pixel block areas included in said pixel block unit is

rewritten for each of a plurality of frames, or each pixel block area remains the same for a plurality of said frames.

12. (currently amended)The image display system according to claim 11, wherein said block discrimination circuit portion discriminates ~~the state of whether~~ said image information is at least ~~the a~~ moving picture or ~~the a~~ still picture in said pixel block unit.

13. (currently amended)An image display system, comprising:  
an image display unit;  
a receiving portion for receiving ~~the~~ image information;  
a storage portion for storing ~~the~~ said image information received by said receiving portion; and

a synchronizing signal generation portion for reading ~~the~~ said image information from said storage portion, controlling ~~the a~~ clock in accordance with said ~~the~~ read image information, and outputting ~~the~~ read image information to said image display unit;

wherein said image processing of said image information before being display is conducted by processing said pixel block unit such that either each of a plurality of pixel block areas included in said pixel block unit is rewritten for each of a plurality of frames, or each pixel block area remains the same for a plurality of said frames.

14. (currently amended)An image information transmission method,  
comprising the steps of:  
discriminating a state of image information amounting to one frame among the  
image information in a pixel block unit;  
processing said pixel block unit including the said image information based on  
the basis of the a discriminated result;  
storing the said image information which has been processed; and  
reading the said image information, controlling the a clock in accordance with  
said the read image information, and outputting the read image information-  
wherein said processing processes said pixel block unit such that either each  
of a plurality of pixel block areas included in said pixel block unit is rewritten for each  
of a plurality of frames, or each pixel block area remains the same for a plurality of  
said frame.

15. (currently amended)The image information transmission method  
according to claim 14, wherein the state of image information amounting to one  
frame among the image information is the state of at least the a moving picture or  
the a still picture, and further comprising discriminating the state of the moving  
picture or the still picture in said pixel block unit.

16. (original) The image information transmission method according to  
claim 14, wherein said image information is the received information.

Claims 17-22 (canceled).